

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



PATENTS

In re application of

Yoshiaki YAMAMOTO

Serial No. 09/089,666

GROUP 2812

Filed June 3, 1998

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IDS  
Q-13-99  
A.Carty

METHOD OF FABRICATING SEMICONDUCTOR  
DEVICE FOR PREVENTING RISING-UP  
OF SILISIDE

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents

Washington, D.C. 20231

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the accompanying documents, copies of which are attached to this statement, are made of record on the enclosed sheet.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in Japanese application Serial No. 9-149733, filed June 6, 1997.

Respectfully submitted,

YOUNG & THOMPSON

By

Robert J. Patch  
Attorney for Applicant  
Registration No. 17,355  
745 South 23rd Street  
Arlington, VA 22202  
Telephone: 703/521-2297

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